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PATENT Attorney Docket No. 00-103 Express Mail Label No. EL662347785US

COMBINED DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As the below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled PROBING FIXTURE AND METHOD FOR SEMICONDUCTOR WAFER, the specification of which is attached hereto, and which is assigned Attorney Docket No. 00-103 by my below-named attorneys.

The person named as inventor is: Margaret S. Fyfield.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose information which is material to patentability as defined in CFR 37 § 1.56.

No priority claim is made under 35 U.S.C. § 119 or 35 U.S.C. § 120.

Power of Attorney: As the named inventor, I hereby appoint Ralph R. Veseli, Registration No. 33,807; Gary E. Ross, Registration No. 29,431; David G. Pursel, Registration No. 28,659; Sandeep Jaggi, Registration No. 43,331; John R. Ley, Registration No. 27,453; and L. Jon Lindsay, Registration No. 36,855, to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Send all correspondence to: Ralph R. Veseli, Esq., Intellectual Property Law Department, LSI Logic Corporation, M/S D-106, 1551 McCarthy Boulevard, Milpitas, CA 95035, and direct telephone calls to Ralph R. Veseli at (408) 433-6404.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

| | Full name of inventor: | Margaret S. Fyfield | |
|------------|------------------------|--|-----------------------------------|
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| | | | |
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